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Terms	Documents
L9 and memory	1

Database:

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side by side**Hit Count Set Name**
result set*DB=USPT; PLUR=YES; OP=OR*

<u>L10</u>	L9 and memory	1	<u>L10</u>
<u>L9</u>	L8 and interface	1	<u>L9</u>
<u>L8</u>	L7 and pixel	1	<u>L8</u>
<u>L7</u>	6191758	1	<u>L7</u>
<u>L6</u>	L4 and auxiliary	0	<u>L6</u>
<u>L5</u>	L4 and pixel	0	<u>L5</u>
<u>L4</u>	L3 and memory	2	<u>L4</u>
<u>L3</u>	6295038	2	<u>L3</u>
<u>L2</u>	L1 and photomask	4	<u>L2</u>
<u>L1</u>	((345/107)!.CCLS.) and electrophoretic	127	<u>L1</u>

END OF SEARCH HISTORY

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Terms	Documents
L23 and (microcontroller or microprocessor)	6

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I25

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result set

DB=USPT; PLUR=YES; OP=OR

<u>L25</u>	L23 and (microcontroller or microprocessor)	6	<u>L25</u>
<u>L24</u>	L23 and wiper	0	<u>L24</u>
<u>L23</u>	L21 and joystick	6	<u>L23</u>
<u>L22</u>	L21 and TTL	0	<u>L22</u>
<u>L21</u>	L20 and threshold	6	<u>L21</u>
<u>L20</u>	L18 and program\$3	6	<u>L20</u>
<u>L19</u>	L18 and CMOS	2	<u>L19</u>
<u>L18</u>	L17 and voltage	6	<u>L18</u>
<u>L17</u>	L16 and firmware	6	<u>L17</u>
<u>L16</u>	L15 and discharg\$3	13	<u>L16</u>
<u>L15</u>	L14 and charg\$3 with capacitor	15	<u>L15</u>
<u>L14</u>	((345/161)!.CCLS.) and potentiometer	146	<u>L14</u>
<u>L13</u>	((345/1.1)!.CCLS.)	181	<u>L13</u>
<u>L12</u>	L10 and cable	0	<u>L12</u>
<u>L11</u>	L10 and ribbon	0	<u>L11</u>
<u>L10</u>	L9 and memory	1	<u>L10</u>
<u>L9</u>	L8 and interface	1	<u>L9</u>
<u>L8</u>	L7 and pixel	1	<u>L8</u>
<u>L7</u>	6191758	1	<u>L7</u>
<u>L6</u>	L4 and auxiliary	0	<u>L6</u>
<u>L5</u>	L4 and pixel	0	<u>L5</u>
<u>L4</u>	L3 and memory	2	<u>L4</u>
<u>L3</u>	6295038	2	<u>L3</u>
<u>L2</u>	L1 and photomask	4	<u>L2</u>
<u>L1</u>	((345/107)!.CCLS.) and electrophoretic	127	<u>L1</u>

END OF SEARCH HISTORY